



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: Hapke, Friedrich

Application No.: 10/090348 Conf.: 3969

Date Filed: 03/04/2002

Notice of Allowance Date: 03/15/2004

Docket No.: DE01 0056

Art Unit: 2829

Examiner: Hollington, Jermele M.

Title: Arrangement And Method Having A Data Word Generator For Testing Integrated Circuits

Mail Stop Issue Fee  
Commissioner for Patents  
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- ✓ Issue Fee Transmittal – ALL required fees paid by deposit account
- ✓ Letter to Official Draftsman
- ✓ Two (2) sheets of Formal Drawings

  
(Signature)

Daniel L. Michalek

(Typed or printed name of person signing Certificate)

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TESTING INTEGRATED CIRCUITS

Box ISSUE FEE  
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**LETTER TO OFFICIAL DRAFTSMAN**

Sir:

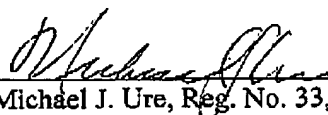
Enclosed please find two (2) sheets of corrected/substitute drawings for entry in the  
above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any  
overpayment to Deposit Account No. 14-1270.

Date: 6/15/04

Respectfully submitted,

By

  
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